

LONGLINE PC MEMORY DDR3 8GB 1600MHZ PC3-12800 LNGDDR316001024DT/8GB

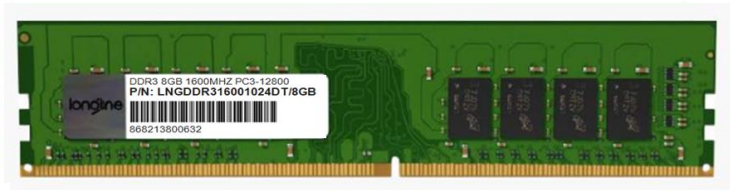
8GB 1600MHZ PC3-12800 1024MX64 8C 1.5V CL11

DESCRIPTION

This document describes LonglineRAM's DDR3 8GB 1600MHZ PC3-12800 1024MX64 8C 1.5V CL11, memory module, based on sixteen 1024M x 64-bit FBGA components. The SPD is programmed to JEDEC standard latency DDR3-1600 timing of 11-11 -11 at 1.5V. This 240-pin DIMM uses gold contact fingers. The electrical and mechanical specifications are as follows:

FEATURES

- JEDEC standard 1.5V (1.425V -1.575V) Power Supply
- VDDO = 1.5V (1.425V 1.575V)
- 800MHz fCK for 1600Mb/sec/pin
- 8 independent internal banks
- Programmable CAS Latency: 11, 10, 9, 8, 7, 6
- Programmable Additive Latency: 0, CL-2, or CL-1 ClOck
- Module configuration 1024Mx64
- Burst Length: 8 (Interleave without any limit, sequential with starting address "000" only), 4 with tCCD = 4 which does not allow seamless read or write [either on the fly using A12 Or MRS]
- Bi-directional Differential Data Strobe
- Internal(self) calibration: Internal self calibration through ZO pin (RZO : 240Ohm* 1%)
- On Die Termination using ODT pin
- Average Refresh PeriOd 7.8us at lower than TCASE 85°C, 3.9us at 85°C < TCASE < 95°C
- Asynchronous Reset
- PCB: Height 0.740" (18.75mm) or 1.180" (30.00mm)



SPECIFICATIONS

CL(IDD)	11 cycles
ROW Cycle Time (tRCmin)	48.125ns (min.)
Refresh to Active/Refresh Command Time (tRFCmin)	260ns (min.)
ROW Active Time (tRASmin)	35ns (min.)
Maximum Operating Power	TBD W*
UL Rating	94 V-0
Operating Temperature	0° C to 85° C
Storage Temperature	-55° C to +100° C

*Power will vary depending on the SDRAM used.